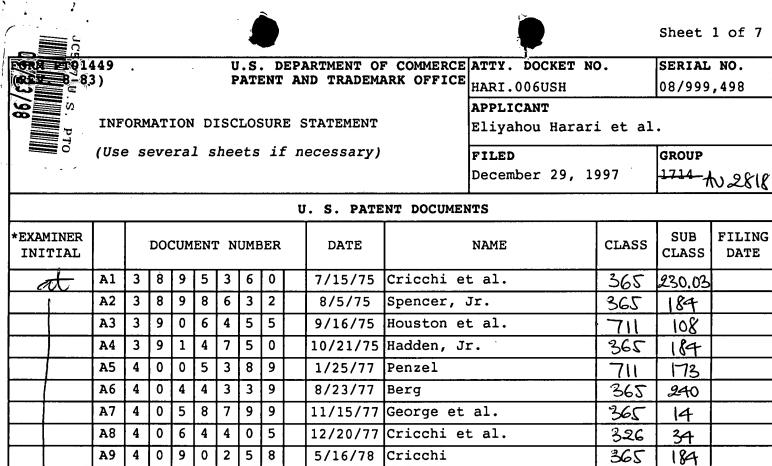
707

365

184



## FOREIGN PATENT DOCUMENTS

12/19/78 Adam

9/19/78 Houston et al.

		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUB CLASS	TRANS.? (YES/NO
at	B1	0	2	8	3	2	3	8		9/21/88	Europe			20
1	B2	5	2	8	7	3	8			1/22/77	Japan	-		
	В3	5	8	8	6	7	7	7		5/24/83	Japan			
	В4	5	8	2	1	5	7	9	4	12/15/83	Japan			5
at	В5	5	8	2	1	5	7	9	5	12/15/83	Japan	-		YE6

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

at		J.E. Brewer et al., "Block-Oriented Random Access MNOS Memory," National Computer Conference and Exposition, Chicago, May 6-10, 1974, AFIPS Conference Proceedings, Vol. 43, pp. 837-840	
		J.E. Brewer et al., "Low Cost MNOS BORAM," Proceedings of IEEE National Aerospace Electronics Conference, NAECON '77, May 17-19, 1977, pp. 6-24-626	ı
		J.R. Cricchi et al., "Nonvolatile Block-Oriented RAM," IEEE International Solid- State Circuits Conference, Feb. 13-15, 1974, Digest of Technical Papers pp. 204-	
at	C4	Raul-Adrian Cernea et al., "A 1Mb Flash EEPROM," 1989 IEEE International Solid- State Circuits Conference	

EXAMINER

A10

A11

at

4 1 1 5 8 5 0

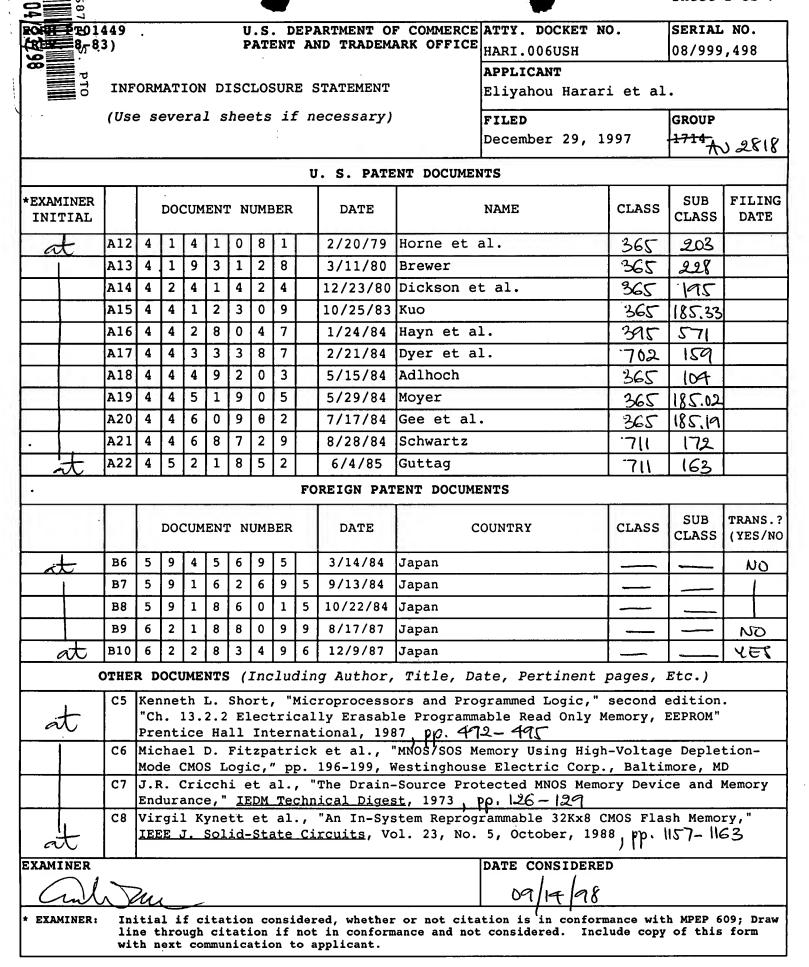
> 9 0

3 0

DATE CONSIDERED

\* EXAMINER:

Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Sheet 3 of 7 FORM P#01449 U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. SERIAL NO. (Reff 8-83) PATENT AND TRADEMARK OFFICE HARI.006USH 08/999,498 ≣ ⊂. %**■**.s APPLICANT INFORMATION DISCLOSURE STATEMENT Eliyahou Harari et al. (Use several sheets if necessary) FILED GROUP 1714 AU 2818 December 29, 1997 U. S. PATENT DOCUMENTS \*EXAMINER SUB FILING DOCUMENT NUMBER DATE NAME CLASS CLASS DATE INITIAL A23 6/4/85 5 2 1 8 5 3 Guttag 711 163 at A24 4 5 2 2 5 7 7/2/85 Cricchi 184 365 A25 2 9 8 2/10/87 Rao 1852 A26 6 5 0 8/4/87 8 Canepa ३६८ 230.08 A27 5 8 9 8 6 7/19/88 Kuo 365 186 A28 7/19/88 4 7 5 8 9 8 8 Kuo 365 185.M A29 7 3 3 0 5 8/9/88 Kuo 4 6 365 185.22 A30 8 0 8 5 10/25/88 | Iida et al. 365 185,22 A31 7 9 6 2 3 5 1/3/89 Sparks et al. 365 189.07 A32 8 0 5 1 5 2/14/89 Terada et al. 371 21.1 8 2 3 3/7/89 A33 1 1 Knothe et al. 365 195 A34 1 2 3/7/89 Kobayashi et al. 365 185.22 8 2 2 A35 1 4/11/89 Nakamura et al. 195 A36 9 7 0 5 4 8 3 6/13/89 Tigelaar et al. 365 185.16 A37 4 8 4 4 2 Kreifels et al. 1 8 6/20/89 365 185.22 **A38** 2 2 8 0 8/22/89 |Carroll 6 365 185.09 FOREIGN PATENT DOCUMENTS SUB TRANS.? DOCUMENT NUMBER DATE COUNTRY CLASS CLASS (YES/NO B11 6 2 8 3 12/9/87 atJapan NO B12 6 1 8 5 9 6 1/26/88 Japan **B13** 5/12/88 3 1 0 6 9 8 6 Japan B14 3 1 8 3 7/29/88 Japan

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

Japan

C9 Denny Cormier, "Erasable/Programmable Solid-State Memories," EDN, pp. 145-154, November 14, 1985

9/20/88

EXAMINER

at

3 | 2

9

**B15** 

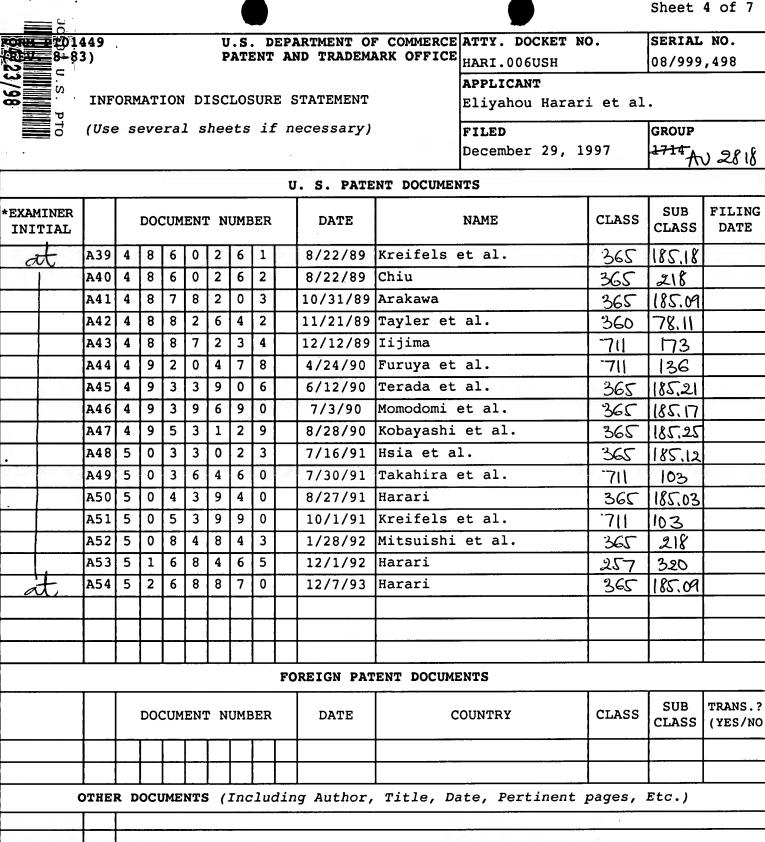
DATE CONSIDERED

BU

09/19/98

\* EXAMINER:

Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

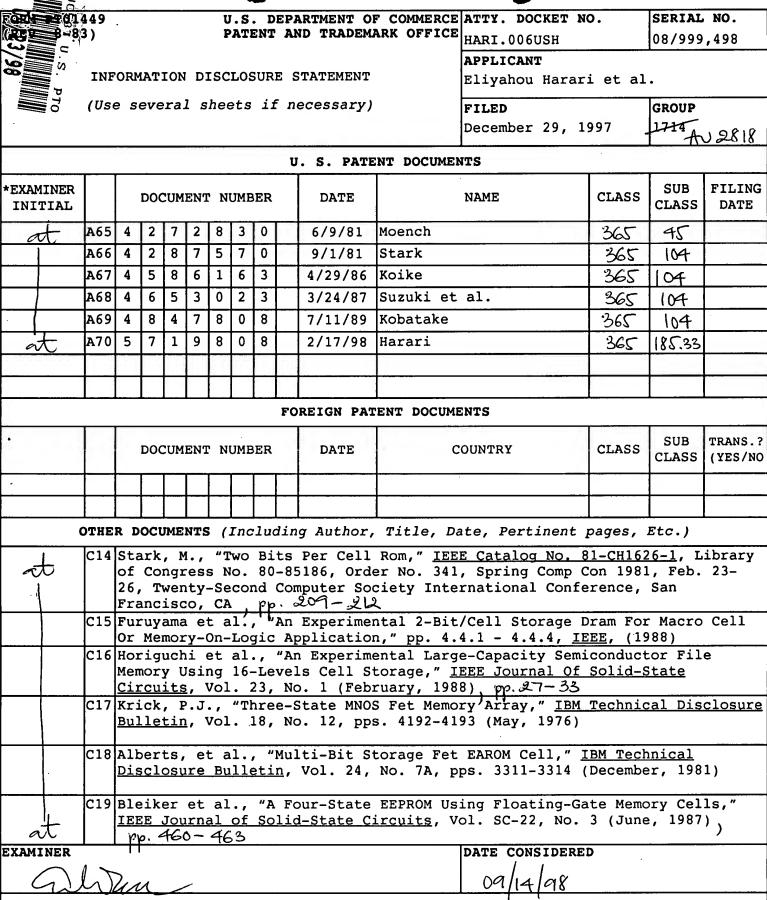


EXAMINER

Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

DATE CONSIDERED

													ATTY. DOCKET NO	SERIAL NO.			
	483	)					PA!	ren	T AN	ID ?	IRADEMARK (	OFFICE	HARI.006USH		08/999,498		
(S)													APPLICANT				
70		INFO	RM	ITA	ON	DIS	CL	OSU	RE S	TAT	CEMENT		Eliyahou Harar:	i et al	•		
	(	(Use	se	vei	al	sh	eet	s i	f ne	ece	ssary)		FILED		GROUP		
	December 29, 1997											1717 AV 2818					
									บ	. s	. PATENT D	OCUME	NTS				
*EXAMINI				DC	CUI	MEN	T N	UME	BER		DATE		NAME	CLASS	SUB CLASS	FILING DATE	
at		A55	4	0	9	9	0	6	9		7/4/78	Cricc	hi et al.	365	218		
1		A56	4	9	3	1	9	9	7		6/5/90	Mitsu	ishi et al.	365	185.04		
	$\neg$	A57	4	7	5	2	8	7	1		6/21/88	Spark	s et al.	365	185.11		
		A58	4	9	7	0	6	9	2		11/13/90	Ali e	t al.	365	230.06		
		A59	4	9	4	9	3	0	9		8/14/90	Rao		365	185.12		
		<b>A</b> 60	5	4	1	8	7	5	2		5/23/95	Harar	i et al.	365	218		
		A61	5	0	9	5	3	4	4		3/10/92	Harar	i	257	328		
		A62	5	1	6	3	0	2	1		11/10/92	Mehro	tra et al.	365	185.03		
		A63	5	1	7	2	3	3	8		12/15/92	Mehro	tra et al.	365	185.03		
at		A64	5	4	3	4	8	2	5		7/18/95	Harar	i	365	185.24		
			T						FO	REI	GN PATENT	DOCUME	ENTS				
	,	DOCUMENT NUMBER									DATE		COUNTRY	CLASS	SUB CLASS	TRANS.	
	0	THER	DC	CUI	MEN	TS	(II	ıclı	ıdin	g A	Author, Tit	le, Da	ate, Pertinent	pages,	Etc.)		
at			IEI	EE .	Jou		1 0						AND PROCESS- TO Vol. SC-20, No.			PROM,	
		C11	In	tel	Co		rat			7F2	256 256K (3	32K x	8) CMOS FLASH M	EMORY,	DATA SI	HEET,	
	$\neg$		SEI	EQ	Tec	hnc	log	JY :	Inco	-	•		LASH EEPROM - P	RELIMIN	ARY DAT	ra	
	-	C12									o. 2-1 - 2-		eir role in sys	tem des	ian "		
at		C13									1, 1989)			cem des			
EXAMINE	ER	Zu.	·										DATE CONSIDERE	D		:	
* EXAMINE	R:	thro	ough	cit	atio	ation on i: o ap	f no	t in	conf	whe	ether or not c	itation onsidere	is in conformance wi	th MPEP 6	09; Draw with nex	line t	



\* EXAMINER:

Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO.

HARI.006USH

SERIAL NO.

08/999,498

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT

Eliyahou Harari et al.

FILED

**GROUP** 

December 29, 1997

1714 AU 2818

## U. S. PATENT DOCUMENTS

*EXAMINER INITIAL DOCUMENT NUMBER DATE NAME CLASS SUB CLASS FINITIAL A71 5 0 9 5 4 6 1 3/10/92 Miyakawa et al.  A72 5 2 7 0 9 7 9 12/14/93 Harari et al.  A73 5 2 7 0 9 8 0 12/14/93 Pathak et al.	
A72 5 2 7 0 9 7 9 12/14/93 Harari et al.	FILING DATE
A73 5 2 7 0 9 8 0 12/14/93 Pathak et al.	
A74 5 2 7 4 5 9 9 12/28/93 Ema	
A75 5 2 9 7 0 9 6 3/22/94 Terada et al.	
A76 5 3 8 4 7 4 3 1/24/95 Rouy	
A77 5 3 8 8 0 8 3 2/7/95 Assar et al.	
A78 5 6 1 9 4 5 1 4/8/97 Costabello et al.	

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUB CLASS	TRANS.? (YES/NO
at	B16	0	3	9	2	8	9	5	10/17/90	European Pat. Off.			NO
at	B17	4	1	1	9	3	9	4	12/19/91	Germany			ΝÖ

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

C20 Nakayama et al. "A 60ns 16Mb Flash EEPROM With Program and Erase Sequence at Controller". IEEE Journal of Solid-State Circuits, Vol. 26, No. 11, Nov. 1991, New York US, pp. 1600-1605

EXAMINER

DATE CONSIDERED

EXAMINER:

Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.